In the Specification:

Please replace the last paragraph located on Page 10, line 24 - Page 11, line 12, with the following amended paragraph:

The application of the tapered reset waveform to the composite reset amplifier enables

tapered reset, the row is resettable to tens of microseconds for full noise suppression, or shorter time for moderate noise reduction. U.S. Patent Application Serial No. 09/057,423 (assignee docket number 97SC087), entitled "COMPACT LOW NOISE ACTIVE PIXEL SENSOR WITH PROGRESSIVE ROW RESET" filed on April 8, 1998, U.S. Patent No. 6,697,111, entitled "COMPACT LOW-NOISE ACTIVE PIXEL SENSOR WITH PROGRESSIVE ROW RESET", issued Feb. 24, 2004, the disclosure of which is herein incorporated by reference, describes the generalized small-signal equivalent circuit model during reset. This circuit allows calculation of the steady-state noise envelope at the reset node depending on reset switch resistance, R_{sw}. If the reset voltage is ramped down too slowly, too much time is needed to reset each row and operation at video frame rates can become problematic. If the tapered-reset

waveform is instead ramped down too quickly, then the kTC noise envelope will not decay

sufficiently to suppress reset noise before the switch is completely opened.

